AB_BIAS_DIFF_Thd_MX * www.idea2ic.com

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*				91 ap														
VCC VAC1 IB1 IB2 CN1	====== VC VIN 0 0	0 0 VBN1 VBN2		DC DC 1u 1u	10 0	SIN(0	1		1000))							
QN2 QN3	VBN1 VBN1 VCN3	VBN1 VBN1 VBN1	VE4 VE3 VE3	NPN1 NPN1 NPN1	1.00))0												
QN4 QN5 <mark>QN6</mark>	VCN4 VBN2 VBN2	VBN2 VBN2 VBN2	VE4 VE4 VE3	NPN1 NPN1 NPN1	15.0 1.00 4.70) ()))												
QP1 QP2 R3 R4	0 0 VCN3 VCN4	VBP1 0 VC VC	VE3 VE4 1K 1K	PNP1 PNP1	1.00)												
E_DIF ROUT E_GAININ	OUT OUT VBP1	0 0 0	VCN3 1K VIN	VCN4 0	1 1m													
.control tran plot echo setplot set compose compose 250m settype let let		25u out "THD% new NameLis NxVals VinVals voltage NoOfNx NoOfVin	versus st = (s v e V = 1 n = 1	VIN_vpk alues alues inVals ength(N: ength(V:	5m " Mx4 10m wVals	Mx4pt25 4.25 30m 35m 5) Ls)	0 Mx4r 4.5 40m	1u 55 Mx5 60m 70	5 j) 80m 1	.00m	110m	120m	140m	150m	170m	185m	200m
begin unset * =======		interru =Loop N	upt K======															
let while let alter alter set let		j (j Nx QN1 an QN6 an thisNar \$thisNa	= <= rea = rea = ne =	1 NoOfNx) NxVals[] \$&Nx \$&Nx \$&Nx \$NameLis 0*vector) j-1] st[\$& c(NoC	ij] OfVin)												

```
Loop_Vin=
let
                  k
                                 = 1
                                <= NoOfVin )
while
                  ( k
let
                  Vin
                                 = VinVals[k-1]
alter
                  e gainin gain = $&Vin
                  2<u>5</u>u
tran
                                     5m
                                               0
                                                     1u
linearize
                                 "blackman"
                  specwindow=
set
                  200
spec
                         8k
                                  200
                                           v(out)
                  thdsq =mag(out[9])^2 +mag(out[14])^2 +mag(out[19])^2 +mag(out[24])^2
let
                  thd_percent= 100*sqrt(thdsq)/mag(out[4])
"$&unknown.Vin $&thd_percent"
unknown.{$thisName}[unknown.k-1] = thd_percent
let
echo
let
                  3
repeat
destroy
end
if
                  ($?interrupt)
goto
                  bail
endif
                  k =
                                k + 1
let
endwhile
setscale
                  VinVals
plot
                  $NameList loglog title "AB_BIAS THD_% vs Vin_pK and Mx"
let
                  j =
                                j + 1
endwhile
label
                  bail
echo
                 "Done."
end
.endc
BF=2100 VAF=216 )
.model
          NPN1
                  NPN (
.model
           PNP1
                  PNP (
                          BF=2100 VAF=21)
```

```
.end
```

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This simulation only works on MacSpice for now. Data in spice apparently gets stored in vectors which are ready to be plotted.

The real goal to a OTA differential input is to actually be able to lower the overall offset and noise while at the same time improving the distortion performance.



A normal input stage actually has its noise defined by the number of electrons that flow through it. To be able to break this barrier, the input stage needs to be AB biased. That means that the differential input stage needs to be able to put out more differential output current than it is drawing DC wise.

A invention above is doing this with two area ratios. The NX ratio term defines a current gain which is possible to take place in output transistors QN3 and QN4. The MX term defines what DC current is present with no input signal.



This simulation tests various sizes of the M area ratio. The larger the value of M, the more the AB_Biased input stage is B biased. Less current will flow with no signal. This will lower the overall effects of both offset and noise. However the input distortion will also be higher because it will introduce a type of crossover distortion.

A smaller value of M is a more A biased input stage. The distortion at low signals is better. But the performance of noise, offset, and maximum input signal are all reduced.

For a M value around 4.5, the 1% distort is about at the 150mV level compared to the 18mV level for a normal differential input stage. The distortion levels for the dual differential input stage are shown below.



There appears to be least a factor of two increase in the magnitude of input voltage signal. But really the dual differential input stage should be thought of as having 1% distortion at 62% of its maximum differential output current. The AB Bias input is at about 75% of the maximum available output current at the same level of distortion.

\varTheta 🔿 🔿 Graph 133 - unknown1452: DUAL_DIFF THD_% vs Vin_pK and Nx