

# =====ONE LINE LOGIC=====

**A GATE REQUIRES ONLY A SINGLE SPICE LINE.**

## OneLineLogic

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\* www.idea2ic.com

```

*=====
VT      Vtime  0      PWL   ( 0 0 1 1 )
B1      A      0      V =   u(sin(3.142*50*v(Vtime)))      -.03
B2      B      0      V =   u(sin(3.142*33*v(Vtime)))      -.06
BAND    VAND   0      V =   u( v(A)*v(B)-.1)      -2.03
BOR     VOR    0      V =   u( v(A)+v(B)-.1)      -2.13
BNOT    VNOT   0      V =   u(.9-v(B))      -2.26
BXOR    VXOR   0      V =   u( v(A)*(.9-v(B)) + v(B)*(.9-v(A))-1)      -2.39
BNAND   VNAND  0      V =   1- u(v(A)*v(B)-.1)      -2.42
BNOR    V NOR  0      V =   1- u(v(A)+v(B)-.1)      -2.55

```

```

.control
set      pensize = 2
tran     .1m .1 0 .1m
plot     v(a) v(b) v(vand) v(vor) v(vnot) v(vxor) v(vnand) v(vnor)
.endc

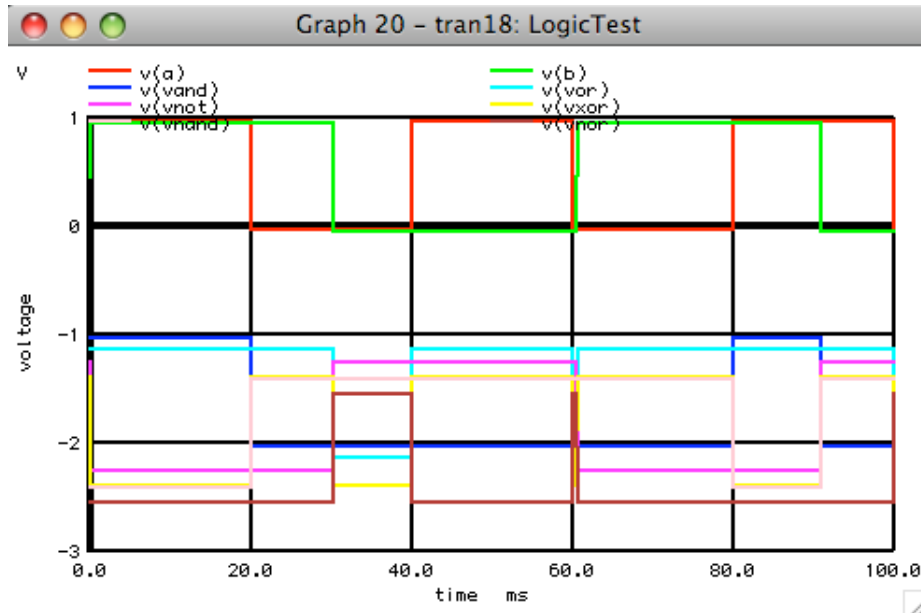
.end

```

=====END\_OF\_SPICE=====

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## Simple One Line Logic



\* #1=====WinSpiceVersion=====